

IN THE CLAIMS

Please cancel, without prejudice, claims 2-22.

1. (Original) A hot patch system for changing of code in a processor comprising, in combination:

a memory for storing a plurality of instructions;

a program counter coupled to the memory for indexing of the memory to access an instruction; and

a cache system coupled to the memory and to the program counter for comparing information associated with the instructions from memory with information stored in the cache system and for altering an instruction stream when there is a comparison match.